



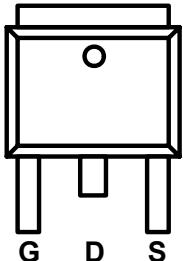
LTD45N06

60V N-CHANNEL TRENCH FET

FEATURES

- $V_{DSS}=60V$
- $I_{DS}=45A @ V_{GS}=10V$
- $R_{DS(ON)}<12m\Omega @ V_{GS}=10V I_D=20A$
- $Q_{g_typ.}=66nC @ V_{GS}=10V$

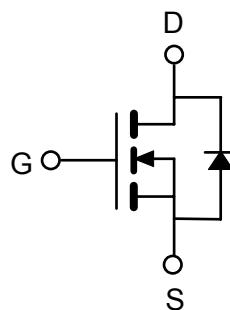
TO-252 D-PAK
Top View



Drain Connected
to Tab

APPLICATION

- Power Supply
- Industrial
- Primary Switch



ABSOLUTE MAXIMUM RATING

($T_A = 25^\circ C$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	Rating	Units
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current ^A	$T_C=25^\circ C$	I_D	45	A
	$T_C=100^\circ C$		28	
Pulsed Drain Current ^C	Pulse	I_{DM}	83	
Single Pulse Avalanche Current ^B	$T_{start}=25^\circ C$	I_{AS}	41	A
Power Dissipation ^A	$T_C=25^\circ C$	P_D	42	W
	$T_C=100^\circ C$		17	
Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150	°C
Single Avalanche Energy ^B	$L=0.3mH$	E_{AS}	250	mJ

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Ambient ^D	$R_{\theta JA}$	60	°C/W
Maximum Junction-to-Case ^D	$R_{\theta JC}$	3	°C/W



TRENCH FET ELECTRICAL CHARACTERISTICS

SPECIFICATIONS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0 \text{ V}, I_D = 250\mu\text{A}$	60			V
Gate-Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.5		3.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48\text{V}, V_{GS} = 0 \text{ V}$			100	nA
		125°C			10	μA
Drain-Source On-Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10 \text{ V}, I_D = 20\text{A}$		10	12	$\text{m}\Omega$
		$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		12	14	
		$V_{GS}=10\text{V}, I_D=20\text{A}, T_J=125^\circ\text{C}$			21	
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{DS}=25\text{V}, V_{GS}=0\text{V}, f=1.0\text{MHz}$		3696		pF
Output Capacitance	C_{OSS}			258		
Reverse Transfer Capacitance	C_{RSS}			104		
Total Gate Charge	Q_g	$V_{DS} = 30 \text{ V}, I_D = 40 \text{ A}, V_{GS}=10\text{V}$		66		nC
Gate-Source Charge	Q_{gs}			16.2		
Gate-Drain Charge	Q_{gd}			13.8		
Switching Characteristics^E						
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=10\text{V}, V_{DD}=48\text{V}, I_D=10\text{A}, R_g=3\text{Ohm}$		15		nS
Rise Time	t_r			14		
Turn-Off Delay Time	$t_{d(off)}$			53		
Fall-Time	t_f			15		
Gate Resistance	R_g		$f=1\text{MHz}$	2.8		Ω
Body Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{GS}=0\text{V}, I_F=20\text{A}$		0.85	1	V
Reverse Recovery Time ^E	t_{rr}	$V_{GS}=0\text{V}, I_F=10\text{A}, dI_F/dt=300\text{A}/\text{us}$		29		ns
Diode Reverse Charge ^E	Q_{rr}			80		nC

Notes:A, Drain current and Power dissipation are based on maximum junction temperature $T_{J(\text{max})}=150^\circ\text{C}$.B, Single pulse UIS energy, inductor=0.3mH, $V_{GS}=10\text{V}$, $T_{start}=25^\circ\text{C}$.C, Pulse width limited by junction temperature $T_{J(\text{max})}=150^\circ\text{C}$, the pulse current value was based on $T_A=25^\circ\text{C}$, repetitive rating based on duty cycles to keep initial $T_J=25^\circ\text{C}$.D, The value of $R_{\theta JA}$ and $R_{\theta JC}$ were measured with device mounted on tested board based on JESD51-7 requirement, and in still air environment with $T_A=25^\circ\text{C}$ in according to JESD51-2.E, Pulse test: $PW \leq 300\text{us}$ duty cycle $\leq 2\%$.

Typical Electrical Characteristics (N-Channel)

$T_A = +25^\circ\text{C}$, unless otherwise noted

Figure 1. On-Regions Characteristics

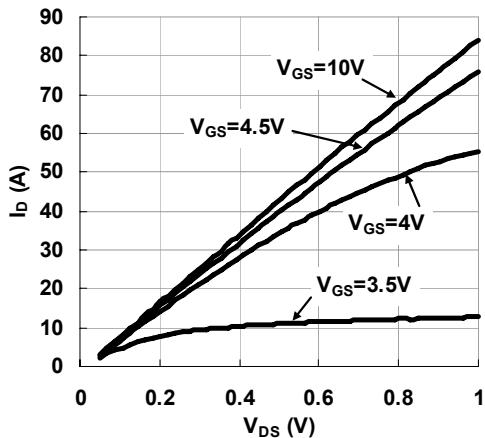


Figure 2. On-Resistance versus Drain Current

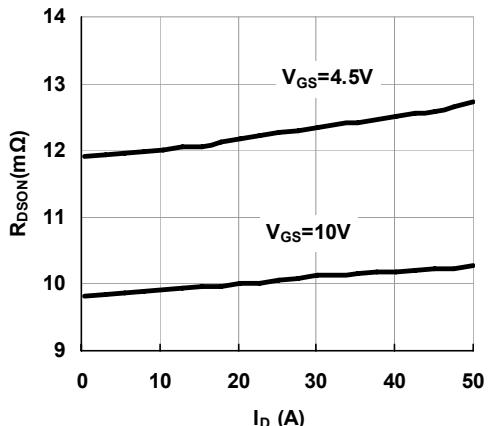


Figure 3. On-Resistance Normalized versus Temperature

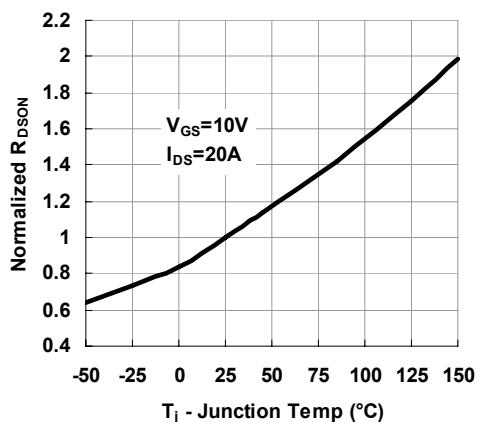


Figure 4. On-Resistance versus Gate to Source Voltage

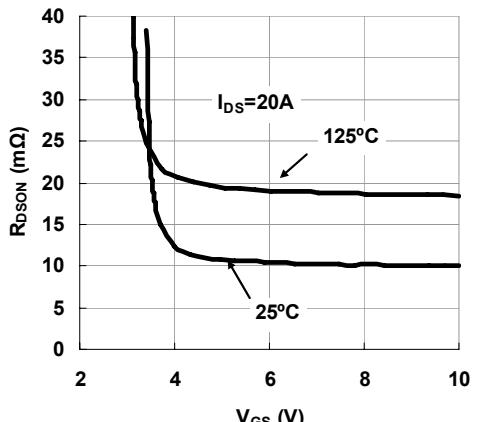


Figure 5. Transfer Characteristics

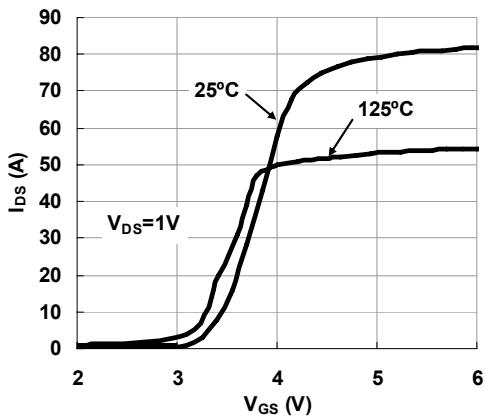
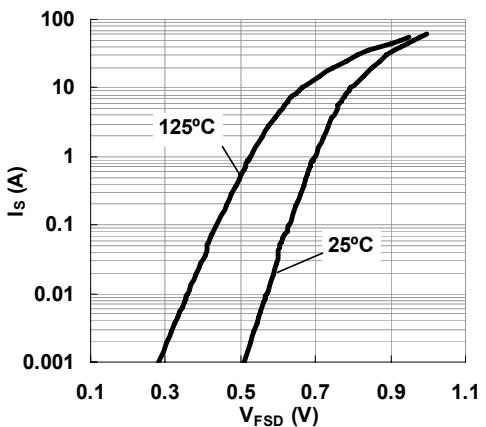


Figure 6. Body Diode Forward Voltage versus Source Current



Typical Electrical Characteristics (N-Channel)

$T_A = +25^\circ\text{C}$, unless otherwise noted

Figure 7. Threshold versus Temperature

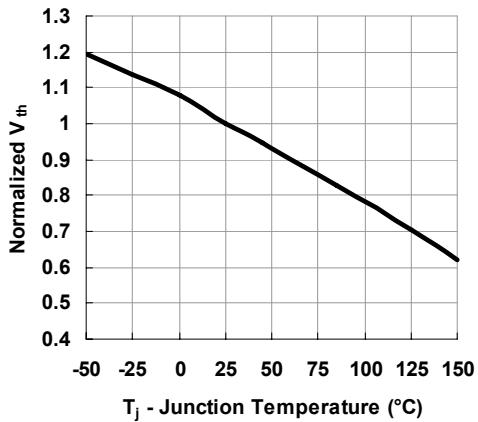


Figure 8. Body Diode Forward Voltage versus Temperature

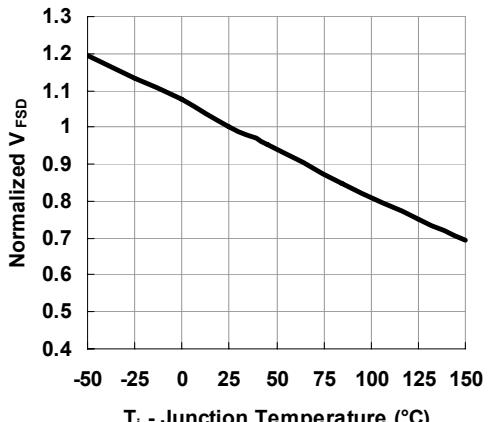


Figure 9. Gate Charge Characteristics

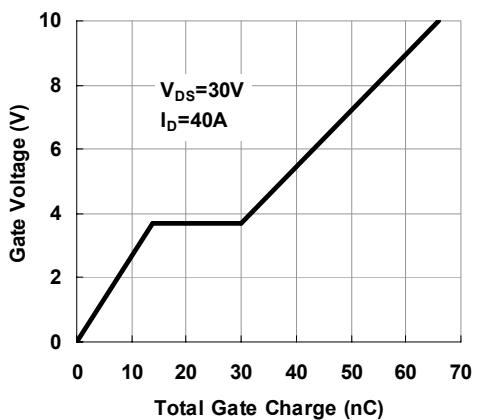
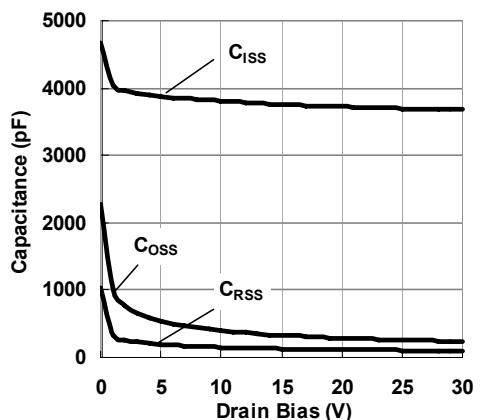
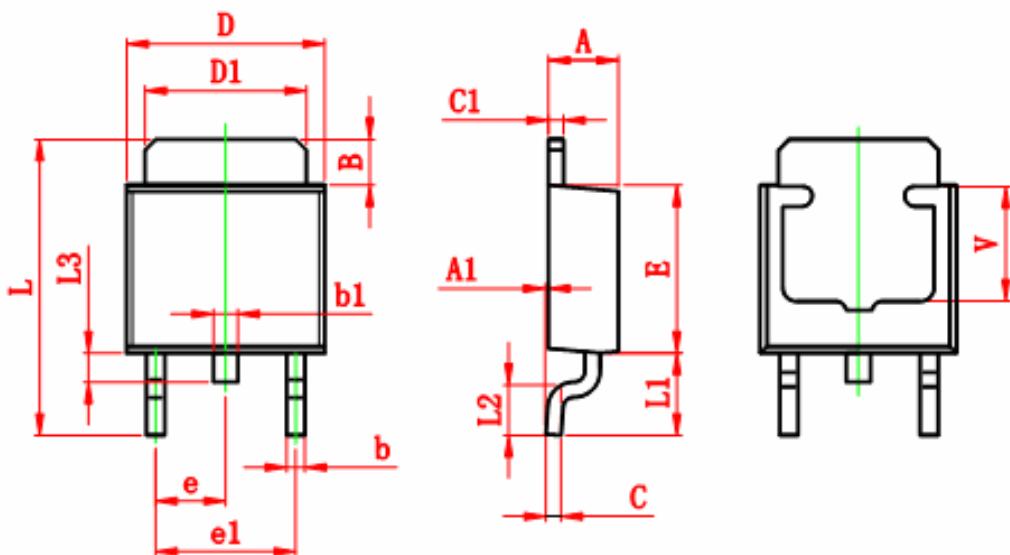


Figure 10. Capacitance Characteristics





TO-220 Package Outline Drawing



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
B	1.350	1.650	0.053	0.065
b	0.500	0.700	0.020	0.028
b1	0.700	0.900	0.028	0.035
c	0.430	0.580	0.017	0.023
c1	0.430	0.580	0.017	0.023
D	8.350	8.650	0.250	0.262
D1	5.200	5.400	0.205	0.213
E	5.400	5.700	0.213	0.224
e	2.300 TYP.		0.091 TYP.	
e1	4.500	4.700	0.177	0.185
L	9.500	9.900	0.374	0.390
L1	2.550	2.900	0.100	0.114
L2	1.400	1.780	0.055	0.070
L3	0.600	0.900	0.024	0.035
V	3.800 REF.		0.150 REF.	